TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS IV)

TPCS8105

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

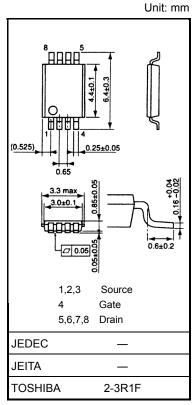
- · Small footprint due to small and thin package
- Low drain-source ON resistance: RDS (ON) = 9.6 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 23 S$ (typ.)
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-30	V	
Drain-gate voltage (Ro	$_{\rm SS} = 20 \text{ k}\Omega$)	V_{DGR}	-30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	ΙD	-10	А	
Diain current	Pulse (Note 1)	I _{DP}			
Drain power dissipation	n (t = 10 s) (Note 2a)	P_{D}	1.1	W	
Drain power dissipation	n (t = 10 s) (Note 2b)	P _D	0.6	W	
Single pulse avalanch	e energy (Note 3)	E _{AS}	26	mJ	
Avalanche current		I _{AR}	-10	Α	
Repetitive avalanche (N	energy lote 2a) (Note 4)	E _{AR}	0.11	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature r	ange	T _{stg}	-55 to 150	°C	

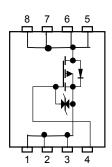
Note 1, Note 2, Note 3 and Note 4: See the next page.

This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 0.035 g (typ.)

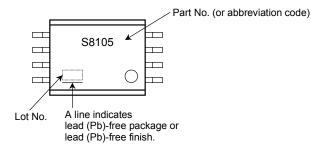
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2a)	R _{th (ch-a)}	114	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	208	°C/W

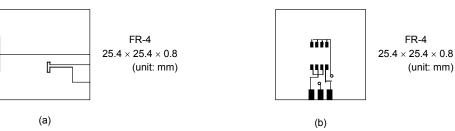
Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:

(a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)

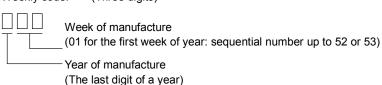


Note 3: $V_{DD} = -24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 0.2 mH, $R_G = 25 \Omega$, $I_{AR} = -10 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: o n lower right of the marking indicates Pin 1.

Weekly code: (Three digits)

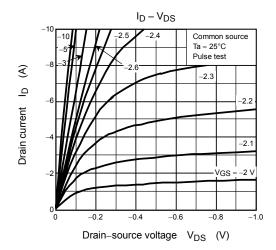


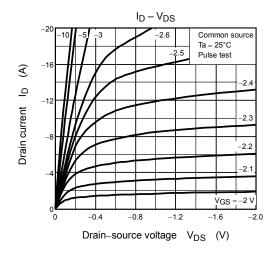
Electrical Characteristics (Ta = 25°C)

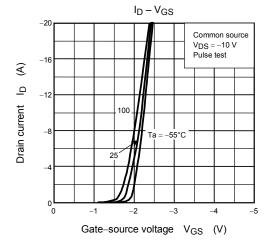
Characte	ristics	Symbol	Test Condition	Min Typ. Max		Unit	
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-OFF current		I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain-source breakdow	un voltago	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V
Diain-source breakdow	wi vollage	V _{(BR)DSX}	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	- ±10 10 -30	V		
Gate threshold voltage		V _{th}	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-0.8	_	-2.0	V
Drain-source ON resist	tanco	Pro (OV)	$V_{GS} = -4 \text{ V}, I_D = -5 \text{ A}$	_	13.5	19.5	mΩ
Dialii-source On resisi	lance	R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -5 \text{ A}$	— — —10 —30 — — —15 — — —0.8 — —2.0 — 13.5 19.5 — 9.6 13.5 11 23 — — 5710 — — 560 — — 590 — — 18 — — 23 — — 109 —	1117.5		
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -5 \text{ A}$	11	23	_	S
Input capacitance		C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	5710	_	pF
Reverse transfer capacitance		C _{rss}		_	560	_	
Output capacitance		Coss		_	590	_	
	Rise time	t _r	V_{CS} 0 V Γ $I_D = -5 \text{ A}$	_	18	_	
Switching time	Turn-ON time	t _{on}	V _{GS} -10 V I _D = -5 A V _{OUT} V _{GS} V _{OUT} V _{GS} V _{GS}	_	23	_	no
Switching time	Fall time	t _f	_	_	109	_	ns
	Turn-OFF time	t _{off}	$V_{DD} \simeq -15 \text{ V}$ Duty $\leq 1\%$, $t_W = 10 \mu\text{s}$	_	396	_	
Total gate charge (gate-source plus gate-	tal gate charge ate-source plus gate-drain)		$V_{DD} \simeq -24 \text{ V}, V_{GS} = 10 \text{ V}.$	_	107	_	nC
Gate-source charge 1		Q _{gs1}	$I_D = -10 \text{ A}$		12		
Gate-drain ("miller") ch	arge	Q _{gd}		_	20		

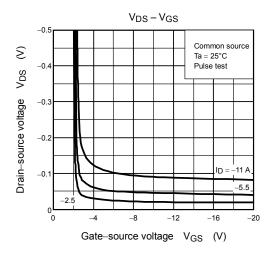
Source-Drain Ratings and Characteristics (Ta = 25°C)

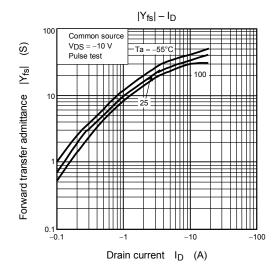
Character	istics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	-40	Α
Forward voltage (diode)			V _{DSF}	$I_{DR} = -10 \text{ A}, V_{GS} = 0 \text{ V}$		_	1.2	V

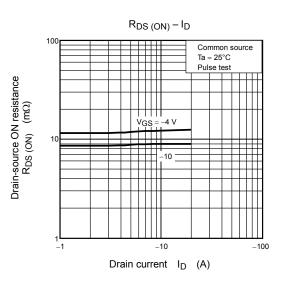


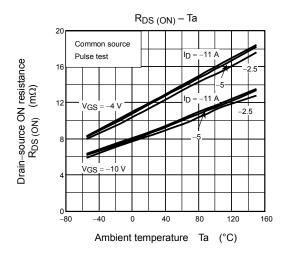


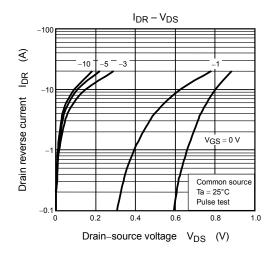


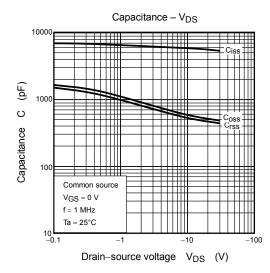


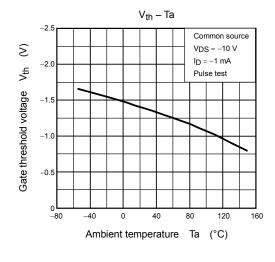


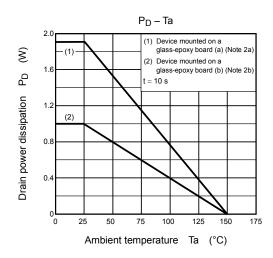


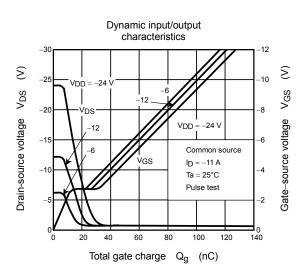


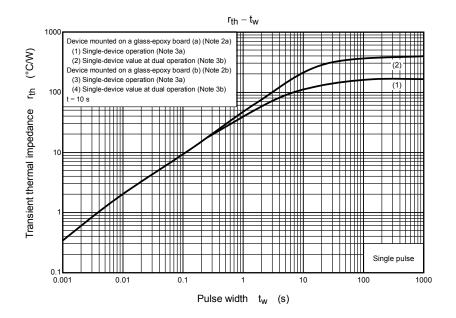


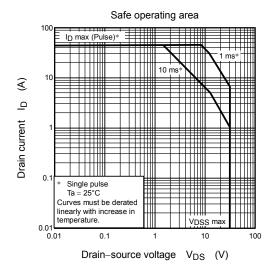












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